

Unified Load-Independent ZPA Analysis and Design in CC and CV Modes of Higher Order Resonant Circuits for WPT Systems

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Abstract—This article proposes a general unified methodology for arbitrary higher order resonant circuits. With the proposed methodology, the equivalent circuits and the general resonant methods of the higher order resonant circuit are presented to realize the load-independent constant current (CC) and constant voltage (CV) outputs at two different load-independent zero phase angle (ZPA) frequencies. In addition, the corresponding regularized mathematical models of the constant output current and voltage and the purely resistive input impedances in CC and CV output modes are derived. All compensation topologies in both inductive and capacitive power transfer (CPT) systems have the essence of higher order resonant circuits. It means that the proposed methodology can be applied to investigate the load-independent output and input characteristics of any inductive power transfer (IPT) and CPT topologies. A 3.3-kW *LCC*-series-compensated IPT system for electric vehicles (EVs) was designed and manufactured to verify the theoretical analysis. The system operating frequencies in both the CC output with ZPA and the CV output with ZPA are in compliance with the SAE J2954 standard.

Index Terms—Capacitive power transfer (CPT), constant current/constant voltage (CC/CV) charging, electric vehicle (EV), higher order resonant circuit, inductive power transfer (IPT), *L*-section matching network, unity power factor.

I. INTRODUCTION

THE wireless power transfer (WPT) technology [1], [2] is becoming widely used in cardiac pacemakers, mobile phones, drones, lighting, and electric vehicles (EVs) [3]–[5]. In a wireless EV charging system, to ensure the safety, durability, and performance of the battery, a constant current/constant voltage (CC/CV) charging profile, regardless of the variation of the battery state of charge (SOC), is one of the most

essential characteristics [6]–[8]. Besides, to enhance the power transfer capability, diminish the volt-ampere (VA) rating, and ensure soft-switching operating condition of the primary inverter, the zero phase angle (ZPA), namely unit power factor, between the primary inverter output current and voltage is necessary [8]–[10].

Usually, a downstream dc–dc converter or a variable frequency control [11] is adopted to regulate the battery load-independent CC or CV output characteristic. However, the additional converter inevitably increases the component counts and associated power losses and costs. For the variable frequency control, the ZPA condition or slightly inductive input impedance is difficult to be achieved over the full range of the charging mode. To achieve load-independent CC output with ZPA or load-independent CV output with ZPA, an inductive power transfer (IPT) topology with a ZPA frequency-tracking control and a back-end converter is proposed [10]. As mentioned previously, however, the additional converter will make the power receiver system bulky and inefficient. Additionally, instability issues due to the frequency bifurcation phenomenon may be inevitable for the ZPA frequency-tracking control [12].

However, the method of designing specified resonant conditions for the compensation topologies in IPT and capacitive power transfer (CPT) systems is an alternative solution. In the IPT systems, the CC output mode at a ZPA frequency can be obtained for the series–series (S-S) [13]–[15], *S-CLC* [16], double-sided *LCL* [17], [18], or double-sided *LCC* [15], [19]–[21]. Moreover, the series–parallel (S-P) [14], double-sided *LCC* [20], [21], and S-SP [22], [23] compensation topologies can realize load-independent CV outputs with ZPA. For these various compensation topologies, however, the analysis and design of the resonant methods are different and irregular. So, a general and simplified method that can systematically analyze the CC and/or CV outputs with ZPA is necessary to be compatible with all kinds of compensation topologies in wireless EV charging applications. In [24] and [25], methods using the basic *LC* network, T-network, and π -network are proposed to investigate the load-independent CC and CV output characteristics of arbitrary topologies. However, the ZPA condition in CC or CV mode needs further comprehensive analysis. In order to analyze the CC output with ZPA operation or the CV output with

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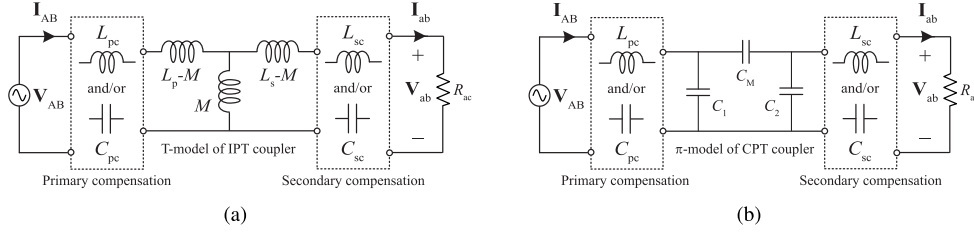


Fig. 1. General configurations of (a) IPT systems and (b) CPT systems.

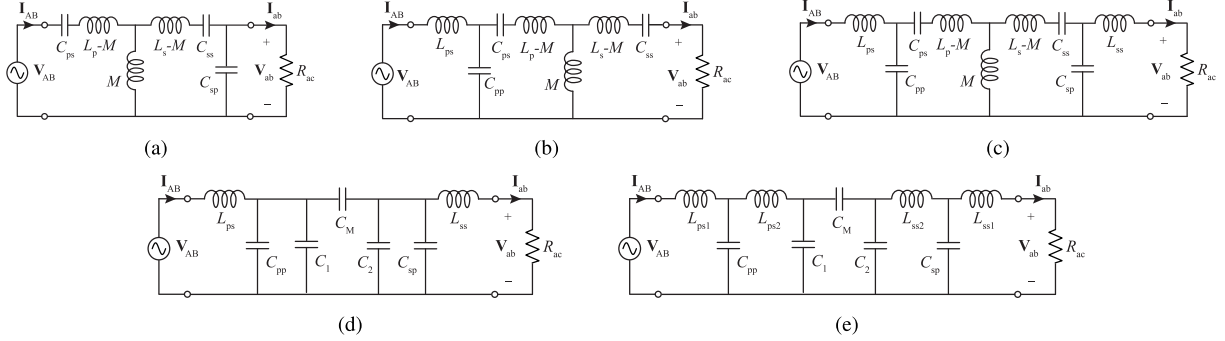


Fig. 2. Some widely used compensation topologies in IPT and CPT systems. (a) S-SP-compensated IPT topology [22], [23]. (b) LCC-series-compensated IPT topology [31], [32]. (c) Double-sided LCC-compensated IPT topology [15], [19]–[21]. (d) Double-sided LC-compensated CPT topology [27]. (e) Double-sided LCLC-compensated CPT topology [33], [34].

ZPA operation of any compensation topologies, a graphical approach method [26] and a family of higher order resonant circuits [16] are proposed. But it is difficult to simultaneously analyze the load-independent CC and CV output modes both with ZPA of arbitrary compensation circuits.

In the CPT systems, there is a paucity of literature on the load-independent output current and voltage and the input ZPA characteristics. The load-independent CC and CV output modes are analyzed and designed for the double-sided LC compensation [27]. In [28], primary π -CLC and secondary T-CLC compensation topologies are proposed to achieve the load-independent CC output mode at a ZPA frequency, while the CV output is realized at a non-ZPA frequency. To promote the application of CPT systems in EVs, more research about the general method analyzing the load-independent CC and CV output modes both with ZPA is also necessary.

To address the aforementioned issues, what we desire, in this article, to accomplish for the dozen compensation topologies in wireless EV charging applications are as follows:

- 1) a general and simplified method that analyze the load-independent CC and CV outputs with ZPA conditions of arbitrary higher order resonant circuits including some widely used compensation topologies in both IPT and CPT systems;
- 2) the regularized mathematical models that express the load-independent output current and voltage and the purely resistive input impedances in CC and CV charging modes.

Specifically, Section II describes the detailed equivalent circuits for an arbitrary higher order resonant circuit and derives the mathematical models. The application of the proposed method in the IPT system is given in Section III. In Section IV, the analytical results are experimentally evaluated. The conclusions are summarized in Section V.

II. LOAD-INDEPENDENT CC AND CV OUTPUT MODES WITH ZPA CONDITIONS OF HIGHER ORDER RESONANT CIRCUITS

A. Fundamental of Wireless Power Transfer System

The general configurations of the IPT system [16], [29] and CPT system [30] are shown in Fig. 1(a) and (b), respectively. In the IPT system, the loosely coupled transformer with the primary and secondary coils is modeled as its T-model. L_p and L_s represent the primary and secondary self-inductances, and M is the mutual inductance. R_{ac} is an ac load. The capacitive coupler in the CPT system is equivalent to its π -model [30]. In IPT and CPT systems, L_{pc} and/or C_{pc} represent the primary compensation components and L_{sc} and/or C_{sc} are the secondary compensation components. Fig. 2 shows some widely used compensation topologies in IPT and CPT systems, for example, an LCC-series-compensated IPT topology [31], [32] shown in Fig. 2(b). From Figs. 1 and 2, it can be seen that all IPT and CPT systems have the essence of higher order resonant circuits.

Fig. 3 shows a general higher order resonant circuit, where X_{S1} , X_{S2} , \dots , $X_{S(m-1)}$, and X_{Sm} represent the series reactances and X_{P1} , X_{P2} , \dots , and $X_{P(m-1)}$ represent the parallel reactances. “ j ” is the unit imaginary number.

B. Basic L-Section Matching Networks

As shown in Fig. 4(a) and (b), respectively, the reversed and normal L-section matching networks [35] are the two types of the most basic resonant circuit. X_{1R} and X_{2R} are the two reactances of the reversed L-section network, and X_{1N} and X_{2N} are the reactances of the normal L-section network.

The output current of the reversed L-section network (I_{abR}) and the output voltage of the normal L-section network (V_{abN})

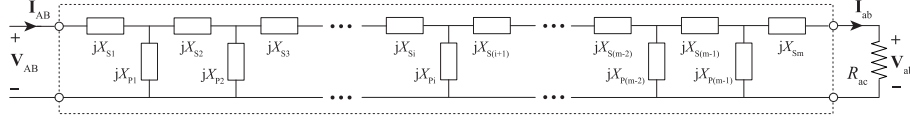
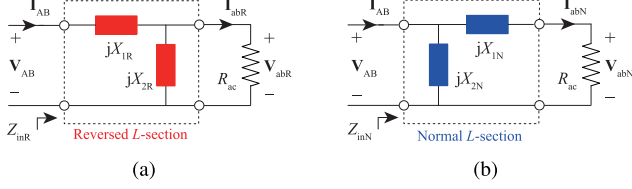


Fig. 3. Arbitrary higher order resonant circuit.

Fig. 4. (a) Reversed L -section matching network. (b) Normal L -section matching network.

are derived as

$$I_{abR} = \frac{jX_{2R}}{R_{ac} \cdot (jX_{1R} + jX_{2R}) + jX_{1R} \cdot jX_{2R}} V_{AB} \quad (1)$$

$$V_{abN} = \frac{R_{ac} \cdot jX_{2N}}{R_{ac} + jX_{1N} + jX_{2N}} I_{AB} \quad (2)$$

where V_{AB} and I_{AB} are the phasors of the input-port voltage and current.

In addition, the input impedances of the reversed and normal L -section matching networks are expressed as

$$Z_{inR} = \frac{R_{ac} \cdot jX_{2R}}{R_{ac} + jX_{2R}} + jX_{1R} = \frac{R_{ac}(jX_{1R} + jX_{2R}) - X_{1R}X_{2R}}{R_{ac} + jX_{2R}} \quad (3)$$

$$Z_{inN} = \frac{(R_{ac} + jX_{1N})jX_{2N}}{(R_{ac} + jX_{1N}) + jX_{2N}} = \frac{(R_{ac} + jX_{1N})jX_{2N}}{R_{ac} + (jX_{1N} + jX_{2N})}. \quad (4)$$

In (1) and (2), when

$$X_{1R} + X_{2R} = 0 \quad (5)$$

$$X_{1N} + X_{2N} = 0. \quad (6)$$

I_{abR} and V_{abN} are independent of the load R_{ac} and can be simplified as

$$I_{abR} = \frac{1}{jX_{1R}} V_{AB} = \frac{1}{-jX_{2R}} V_{AB} \quad (7)$$

$$V_{abN} = jX_{2N} I_{AB} = -jX_{1N} I_{AB}. \quad (8)$$

Moreover, under the conditions of (5) and (6), Z_{inR} and Z_{inN} are, respectively, given by

$$Z_{inR} = \frac{-X_{1R}X_{2R}}{R_{ac} + jX_{2R}} \quad (9)$$

$$Z_{inN} = \frac{(R_{ac} + jX_{1N})jX_{2N}}{R_{ac}}. \quad (10)$$

For the higher order resonant circuit shown in Fig. 3, the following criteria hold.

- 1) The parallel reactances X_{P1} to $X_{P(m-2)}$ in Fig. 3 are modeled as the parallel connection of two reactances, i.e., X_{Pi} is expressed by using X_{PiC1} and X_{PiC2} in parallel. The corresponding equivalent variables are expressed by (11).

- 2) The series reactances X_{S2} to $X_{S(m-1)}$ are equivalent to the series connection of two reactances, i.e., X_{Si} is expressed by X_{SiC1} and X_{SiC2} in series, which is derived in (12)

$$jX_{Pi} = \frac{jX_{PiC1}jX_{PiC2}}{jX_{PiC1} + jX_{PiC2}}, \quad i \in \{1, 2, \dots, (m-2)\} \quad (11)$$

$$jX_{Si} = jX_{SiC1} + jX_{SiC2}, \quad i \in \{1, 2, \dots, (m-1)\}. \quad (12)$$

Fig. 3 is modeled as cascaded connection of $(m-1)$ -stage reversed L -section networks (marked with the red components) and $(m-2)$ -stage normal L -section networks (marked with the blue components), which is shown in Fig. 5(a).

Similarly, the higher order resonant circuit shown in Fig. 3 can also be modeled as shown in Fig. 5(b), and the corresponding variables are expressed as

$$jX_{Pi} = \frac{jX_{PiV1}jX_{PiV2}}{jX_{PiV1} + jX_{PiV2}} \quad \text{and} \quad jX_{Si} = jX_{SiV1} + jX_{SiV2}, \quad i \in \{1, 2, \dots, (m-1)\}. \quad (13)$$

It can be seen that the equivalent circuit shown in Fig. 5(b) consists of $(m-1)$ -stage reversed L -section and $(m-1)$ -stage normal L -section in series.

From Fig. 5(a) and (b), an arbitrary higher order resonant circuit can be modeled as cascaded connection of one or more stages of reversed L -section matching networks and one or more stages of normal L -section matching networks. It means that the two basic types of L -section matching networks can be applied to simplify the analysis and design of the output and input characteristics of any higher order resonant circuits.

C. General Load-Independent CC Output Mode With Load-Independent ZPA

According to the analysis of the reversed and normal L -section matching networks, when the sums of the two reactances of all L -section networks in Fig. 5(a) are designed to be equal to zero, namely,

$$\begin{aligned} X_{S1} + X_{PiC1} \\ &= X_{PiC2} + X_{S2C1} = X_{S2C2} + X_{P2C1} = \dots \\ &= X_{SiC2} + X_{PiC1} = X_{PiC2} + X_{S(i+1)C1} = \dots \\ &= X_{P(m-2)C2} + X_{S(m-1)C1} = X_{S(m-1)C2} + X_{P(m-1)} \\ &= 0 \end{aligned} \quad (14)$$

and the transconductance G_{CC} of Fig. 5(a) can be expressed as

$$\begin{aligned} G_{CC} &= \frac{I_{ab_CC}}{V_{AB}} \\ &= \frac{jX_{P1C2}}{jX_{S1}} \frac{jX_{P2C2}}{jX_{S2C2}} \dots \frac{jX_{PiC2}}{jX_{SiC2}} \\ &\quad \dots \frac{jX_{P(m-2)C2}}{jX_{S(m-2)C2}} \frac{1}{jX_{S(m-1)C2}} \end{aligned} \quad (15)$$

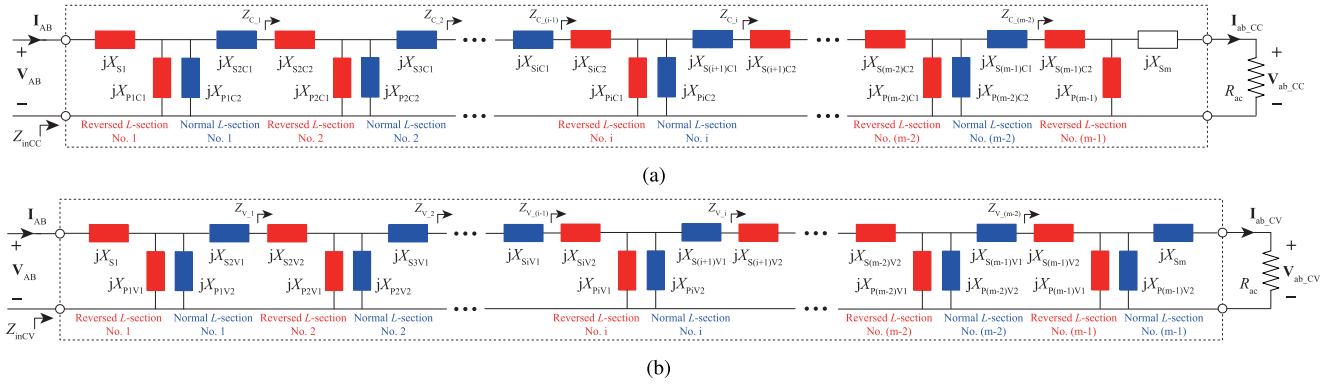


Fig. 5. Two different equivalent circuits of an arbitrary higher order resonant circuit. (a) Equivalent circuit for achieving the CC output with ZPA. (b) Equivalent circuit for achieving the CV output with ZPA.

$$\begin{aligned}
 X_{S1} &= -X_{P1C1} = X_{R_C_1} \text{ (for reversed } L\text{-section No. 1)} \\
 X_{P1C2} &= -X_{S2C1} = X_{N_C_1} \text{ (for normal } L\text{-section No. 1)} \\
 X_{S2C2} &= -X_{P2C1} = X_{R_C_2} \\
 X_{P2C2} &= -X_{S3C1} = X_{N_C_2} \\
 &\vdots \\
 X_{SiC2} &= -X_{PiC1} = X_{R_C_i} \text{ (for reversed } L\text{-section No. } i) \\
 X_{PiC2} &= -X_{S(i+1)C1} = X_{N_C_i} \text{ (for normal } L\text{-section No. } i) \\
 &\vdots \\
 X_{S(m-1)C2} &= -X_{P(m-1)} = X_{R_C_ (m-1)}. \quad (16)
 \end{aligned}$$

From (15), G_{CC} is independent of the load. It means that the load-independent CC output is obtained when the higher order resonant circuit is fed by a CV supply and resonates at the conditions of (14).

In order to simplify and express the regularity of the load-independent CC output characteristic, we define the unified nomenclature of all the variables in Fig. 5(a) as (16) according to (14). In (16), $X_{R_C_i}$ and $X_{N_C_i}$ represent the unified reactances of the i reversed and normal L -section networks, respectively.

Then, the load-independent G_{CC} can be simplified to

$$\begin{aligned}
 G_{CC} &= \frac{I_{ab,CC}}{V_{AB}} \\
 &= \frac{X_{N_C_1}}{X_{R_C_1}} \cdots \frac{X_{N_C_i}}{X_{R_C_i}} \cdots \frac{X_{N_C_ (m-2)}}{X_{R_C_ (m-2)}} \frac{1}{jX_{R_C_ (m-1)}}
 \end{aligned}$$

$$\begin{aligned}
 &= \frac{\prod_{i=1}^{(m-2)} X_{N_C_i}}{j \prod_{i=1}^{(m-1)} X_{R_C_i}}. \quad (17)
 \end{aligned}$$

Furthermore, according to (9), (10), and (16), the input impedance of Fig. 5(a) (Z_{inCC}) is expressed as

$$\begin{aligned}
 Z_{inCC} &= \text{Re}(Z_{inCC}) + j \cdot \text{Im}(Z_{inCC}) = R_{inCC} + j \cdot X_{inCC} \\
 &= \frac{-X_{R_C_1}^2}{(Z_{C_1} - jX_{N_C_1}) \cdot jX_{N_C_1} - jX_{R_C_1}} \quad (18)
 \end{aligned}$$

where

$$\begin{aligned}
 Z_{C_1} &= \frac{-X_{R_C_2}^2}{(Z_{C_2} - jX_{N_C_2}) \cdot jX_{N_C_2} - jX_{R_C_2}} \\
 &\vdots \\
 Z_{C_i} &= \frac{-X_{R_C_ (i+1)}^2}{(Z_{C_ (i+1)} - jX_{N_C_ (i+1)}) \cdot jX_{N_C_ (i+1)} - jX_{R_C_ (i+1)}} \\
 &\vdots \\
 Z_{C_ (m-3)} &= \frac{-X_{R_C_ (m-2)}^2}{(Z_{C_ (m-2)} - jX_{N_C_ (m-2)}) \cdot jX_{N_C_ (m-2)} - jX_{R_C_ (m-2)}} \\
 Z_{C_ (m-2)} &= \frac{-X_{R_C_ (m-1)}^2}{(R_{ac} + jX_{Sm}) - jX_{R_C_ (m-1)}}. \quad (19)
 \end{aligned}$$

$$\begin{aligned}
 &(-X_{R_C_1} + X_{N_C_1})X_{R_C_2} \cdot (-X_{R_C_2}) \cdots X_{R_C_i} \cdot (-X_{R_C_i}) \cdots X_{R_C_ (m-1)} \cdot (-X_{R_C_ (m-1)}) \\
 &+ X_{N_C_1} \cdot (-X_{N_C_1})(-X_{R_C_2} + X_{N_C_2}) \cdots X_{R_C_i} \cdot (-X_{R_C_i}) \cdots X_{R_C_ (m-1)} \cdot (-X_{R_C_ (m-1)}) \\
 &+ \cdots + X_{N_C_1} \cdot (-X_{N_C_1}) \cdots (-X_{R_C_i} + X_{N_C_i}) \cdots X_{R_C_ (m-1)} \cdot (-X_{R_C_ (m-1)}) \\
 &+ \cdots + X_{N_C_1} \cdot (-X_{N_C_1}) \cdots X_{N_C_i} \cdot (-X_{N_C_i}) \cdots (-X_{R_C_ (m-2)} + X_{N_C_ (m-2)})X_{R_C_ (m-1)} \cdot (-X_{R_C_ (m-1)}) \\
 &+ X_{N_C_1}(-X_{N_C_1})X_{N_C_2}(-X_{N_C_2}) \cdots X_{N_C_i}(-X_{N_C_i}) \cdots (-X_{R_C_ (m-1)} + X_{Sm}) \\
 &= \sum_{i=1}^{(m-2)} \left[\prod_{x=1}^{(i-1)} (-X_{N_C_x}^2) \cdot (-X_{R_C_x} + X_{N_C_x}) \cdot \prod_{x=(i+1)}^{(m-1)} (-X_{R_C_x}^2) \right] + (-X_{R_C_ (m-1)} + X_{Sm}) \cdot \prod_{i=1}^{m-2} (-X_{N_C_i}^2) = 0 \quad (20)
 \end{aligned}$$

Z_{inCC} is expected to be purely resistive and independent of the load, that is ZPA operation, which implies that the imaginary part of Z_{inCC} , $\text{Im}(Z_{inCC})$, is always equal to zero for any loading conditions. In order to achieve $\text{Im}(Z_{inCC}) = 0$, the general resonant condition is given by (20), shown at the bottom of the previous page. In this case, the general load-independent purely resistive input impedance (R_{inCC}) is expressed by

$$\begin{aligned} R_{inCC} &= \text{Re}(Z_{inCC}) \\ &= \frac{X_{R_{C-1}}^2 X_{R_{C-2}}^2 \cdots X_{R_{C-i}}^2 \cdots X_{R_{C-(m-2)}}^2 X_{R_{C-(m-1)}}^2}{R_{ac} X_{N_{C-1}}^2 X_{N_{C-2}}^2 \cdots X_{N_{C-i}}^2 \cdots X_{N_{C-(m-2)}}^2} \\ &= \frac{\prod_{i=1}^{(m-1)} X_{R_{C-i}}^2}{R_{ac} \prod_{i=1}^{(m-2)} X_{N_{C-i}}^2} = \frac{1}{R_{ac} |G_{CC}|^2}. \end{aligned} \quad (21)$$

So, for the higher order resonant circuit, the load-independent CC output with the ZPA operation is achieved under the resonant conditions of (16) and (20).

D. General Load-Independent CV Output Mode With Load-Independent ZPA

Similarly, when the sums of the two reactances of all L -section matching networks in Fig. 5(b) are defined as

$$\begin{aligned} X_{S1} + X_{P1V1} &= X_{P1V2} + X_{S2V1} = X_{S2V2} + X_{P2V1} = \cdots \\ &= X_{SiV2} + X_{PiV1} = X_{PiV2} + X_{S(i+1)V2} = \cdots \\ &= X_{S(m-1)V2} + X_{P(m-1)V1} = X_{P(m-1)V2} + X_{Sm} \\ &= 0 \end{aligned} \quad (22)$$

and the voltage transfer ratio G_{CV} can be expressed as

$$\begin{aligned} G_{CV} &= \frac{V_{ab_{CV}}}{V_{AB}} \\ &= \frac{jX_{P1V2}}{jX_{S1}} \frac{jX_{P2V2}}{jX_{S2V2}} \frac{jX_{P3V2}}{jX_{S3V2}} \cdots \frac{jX_{PiV2}}{jX_{SiV2}} \\ &\quad \cdots \frac{jX_{P(m-1)V2}}{jX_{S(m-1)V2}} \end{aligned} \quad (23)$$

$$X_{S1} = -X_{P1V1} = X_{R_{V-1}} \text{ (for reversed } L\text{-section No. 1)}$$

$$X_{P1V2} = -X_{S2V1} = X_{N_{V-1}} \text{ (for normal } L\text{-section No. 1)}$$

$$\vdots$$

$$X_{SiV2} = -X_{PiV1} = X_{R_{V-i}} \text{ (for reversed } L\text{-section No. } i)$$

$$X_{PiV2} = -X_{S(i+1)V1} = X_{N_{V-i}} \text{ (for normal } L\text{-section No. } i)$$

$$\vdots$$

$$X_{S(m-1)V2} = -X_{P(m-1)V1} = X_{R_{V-(m-1)}}$$

$$X_{P(m-1)V2} = -X_{Sm} = X_{R_{V-(m-1)}}. \quad (24)$$

From (23), G_{CV} is independent of the load. It means that the load-independent CV output is realized when the higher order resonant circuit is fed by a CV supply and resonates at the conditions of (22).

Also, in order to simplify and express the regularity of the load-independent CV output characteristic, we define the unified nomenclature of all the variables in Fig. 5(b) as (24) according to (22). In (24), $X_{R_{V-i}}$ and $X_{N_{V-i}}$ represent the unified reactances of the i reversed and normal L -section networks of Fig. 5(b), respectively.

Then, the load-independent G_{CV} is simplified to

$$\begin{aligned} G_{CV} &= \frac{V_{ab_{CV}}}{V_{AB}} = \frac{X_{N_{V-1}}}{X_{R_{V-1}}} \cdots \frac{X_{N_{V-i}}}{X_{R_{V-i}}} \cdots \frac{X_{N_{V-(m-1)}}}{X_{R_{V-(m-1)}}} \\ &= \frac{\prod_{i=1}^{(m-1)} X_{N_{V-i}}}{\prod_{i=1}^{(m-1)} X_{R_{V-i}}}. \end{aligned} \quad (25)$$

Furthermore, according to (9), (10), and (24), the input impedance of Fig. 5(b), Z_{inCV} , is expressed as

$$\begin{aligned} Z_{inCV} &= \text{Re}(Z_{inCV}) + j \cdot \text{Im}(Z_{inCV}) = R_{inCV} + j \cdot X_{inCV} \\ &= \frac{-X_{R_{V-1}}^2}{(Z_{V-1} - jX_{N_{V-1}}) \cdot jX_{N_{V-1}} - jX_{R_{V-1}}} \end{aligned} \quad (26)$$

where

$$Z_{V-1} = \frac{-X_{R_{V-2}}^2}{(Z_{V-2} - jX_{N_{V-2}}) \cdot jX_{N_{V-2}} - jX_{R_{V-2}}}$$

$$\vdots$$

$$Z_{V-i} = \frac{-X_{R_{V-(i+1)}}^2}{(Z_{V-(i+1)} - jX_{N_{V-(i+1)}}) \cdot jX_{N_{V-(i+1)}} - jX_{R_{V-(i+1)}}}$$

$$\vdots$$

$$Z_{V-(m-3)} = \frac{-X_{R_{V-(m-2)}}^2}{(Z_{V-(m-2)} - jX_{N_{V-(m-2)}}) \cdot jX_{N_{V-(m-2)}} - jX_{R_{V-(m-2)}}}$$

$$\begin{aligned} &(-X_{R_{V-1}} + X_{N_{V-1}})X_{R_{V-2}} \cdot (-X_{R_{V-2}}) \cdots X_{R_{V-i}} \cdot (-X_{R_{V-i}}) \cdots X_{R_{V-(m-1)}} \cdot (-X_{R_{V-(m-1)}}) \\ &+ X_{N_{V-1}} \cdot (-X_{N_{V-1}})(-X_{R_{V-2}} + X_{N_{V-2}}) \cdots X_{R_{V-i}} \cdot (-X_{R_{V-i}}) \cdots X_{R_{V-(m-1)}} \cdot (-X_{R_{V-(m-1)}}) \\ &+ \cdots + X_{N_{V-1}} \cdot (-X_{N_{V-1}}) \cdots (-X_{R_{V-i}} + X_{N_{V-i}}) \cdots X_{R_{V-(m-1)}} \cdot (-X_{R_{V-(m-1)}}) \\ &+ \cdots + X_{N_{V-1}} \cdot (-X_{N_{V-1}}) \cdots X_{N_{V-i}} \cdot (-X_{N_{V-i}}) \cdots (-X_{R_{V-(m-2)}} + X_{N_{V-(m-2)}})X_{R_{V-(m-1)}} \cdot (-X_{R_{V-(m-1)}}) \\ &= \sum_{i=1}^{(m-2)} \left[\prod_{x=1}^{(i-1)} (-X_{N_{V-x}}^2)(-X_{R_{V-x}} + X_{N_{V-x}}) \prod_{x=(i+1)}^{(m-1)} (-X_{R_{V-x}}^2) \right] = 0 \end{aligned} \quad (28)$$

TABLE I
SUMMARY OF THE PROPOSED METHODOLOGY FOR AN ARBITRARY HIGHER ORDER RESONANT CIRCUIT

Features	CC output with ZPA	CV output with ZPA
An arbitrary higher-order resonant circuit	Fig. 3	
Equivalent circuits	Fig. 5(a)	Fig. 5(b)
Equivalent variable expressions	(11) and (12)	(13)
Resonant methods for achieving load-independent CC and CV outputs	(16)	(24)
General mathematical models of constant output current and voltage	(17)	(25)
Resonant methods for achieving load-independent ZPA	(16) and (20)	(24) and (28)
General mathematical models of purely resistive input impedances	(21)	(29)

$$Z_{V_{-(m-2)}} = \frac{-X_{R_{-V_{-(m-1)}}}^2}{(R_{ac} - jX_{N_{-V_{-(m-1)}}}) \cdot jX_{N_{-V_{-(m-1)}}} - jX_{R_{-V_{-(m-1)}}}}. \quad (27)$$

Z_{inCV} is expected to be purely resistive and independent of the load, that is the ZPA operation. It means that $\text{Im}(Z_{inCV})$ is always equal to zero. The general resonant method given by (28), shown at the bottom of the previous page, is derived from $\text{Im}(Z_{inCV}) = 0$. In this case, the general load-independent purely resistive input impedance R_{inCV} is expressed as

$$\begin{aligned} R_{inCV} &= \text{Re}(Z_{inCV}) \\ &= \frac{R_{ac} X_{R_{-V_{-1}}}^2 X_{R_{-V_{-2}}}^2 \cdots X_{R_{-C_{-i}}}^2 \cdots X_{R_{-V_{-(m-1)}}}^2}{X_{N_{-V_{-1}}}^2 X_{N_{-V_{-2}}}^2 \cdots X_{N_{-V_{-i}}}^2 \cdots X_{N_{-V_{-(m-1)}}}^2} \\ &= \frac{R_{ac} \prod_{i=1}^{(m-1)} X_{R_{-V_{-i}}}^2}{\prod_{i=1}^{(m-1)} X_{N_{-V_{-i}}}^2} = \frac{R_{ac}}{|G_{CV}|^2}. \end{aligned} \quad (29)$$

So, for the arbitrary higher order resonant circuit, the load-independent CV output with the ZPA operation is achieved under the resonant conditions of (24) and (28).

As aforementioned, from (17) and (25), respectively, the load-independent CC and CV outputs are achieved when the higher order resonant circuit is fed by a CV source system and resonates at the conditions of (16) and (24). Furthermore, from (21) and (29), the input impedances in CC and CV output modes are purely resistive and independent of the load. It means that for a CV-fed higher order resonant circuit, the load-independent CC and CV output modes with ZPA conditions are obtained with the proposed methodology. Table I summarizes the proposed methodology.

III. APPLICATIONS OF THE PROPOSED METHODOLOGY IN WPT SYSTEMS

As mentioned in Section II-A, all compensation topologies in IPT and CPT systems have essence of higher order resonant circuits. So, according to the proposed methodology, we can easily analyze the load-independent CC and CV output modes with ZPA operations for any compensation topologies in IPT and CPT systems. In this article, a CV-fed *LCC*-series topology, which is shown in Fig. 2(b), is applied here in detail to demonstrate the proposed methodology. In Fig. 2(b),

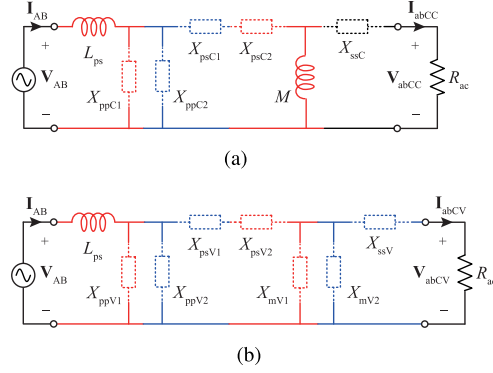


Fig. 6. Equivalent circuits of the *LCC*-series topology for achieving (a) CC output with ZPA and (b) CV output with ZPA.

the primary *LCC* network consists of L_{ps} , C_{pp} , and C_{ps} , and C_{ss} represents the secondary compensation capacitor.

A. Load-Independent CC Mode With ZPA of the *LCC*-Series Topology

According to the analysis in Section II-C, the *LCC*-series topology shown in Fig. 2(b) should be modeled as Fig. 6(a), which is a series-connected circuit of two-stage reversed *L*-section networks, one-stage normal *L*-section network, and the series-connected reactance X_{ssC} . The detailed equivalent expressions are derived by

$$j\omega_{CC}C_{pp} = \frac{1}{jX_{ppC1}} + \frac{1}{jX_{ppC2}} \quad (30a)$$

$$\frac{1}{j\omega_{CC}C_{ps}} + j\omega_{CC}(L_p - M) = jX_{psC1} + jX_{psC2} \quad (30b)$$

$$j\omega_{CC}(L_s - M) + \frac{1}{j\omega_{CC}C_{ss}} = jX_{ssC} \quad (30c)$$

where ω_{CC} ($\omega_{CC} = 2\pi f_{CC}$) represents the resonant frequency of Fig. 6(a).

According to (16) and (17), (31) can be derived and used as the resonant methods of Fig. 6(a) to achieve the load-independent CC output. In this case, the load-independent G_{CC} is given by (32)

$$\omega_{CC}L_{ps} + X_{ppC1} = X_{ppC2} + X_{psC1} = X_{psC2} + \omega_{CC}M = 0 \quad (31)$$

$$G_{CC} = \frac{I_{abCC}}{V_{AB}} = \frac{jX_{ppC2}}{j\omega_{CC}L_{ps}} \frac{1}{jX_{psC2}}. \quad (32)$$

Furthermore, according to (20), the resonant condition for achieving ZPA condition and the corresponding purely resistive input impedance of Fig. 6(a) are expressed as

$$(X_{ppC1} + X_{ppC2})X_{psC2}(\omega_{CC}M) + X_{ppC2}X_{psC1}(\omega_{CC}M + X_{ssC}) = 0 \quad (33)$$

$$R_{inCC} = \text{Re}(Z_{inCC}) = \frac{\omega_{CC}^2 L_{ps}^2 X_{psC2}^2}{R_{ac} X_{ppC2}^2} = \frac{1}{R_{ac} |G_{CC}^2|}. \quad (34)$$

B. Load-Independent CV Mode With ZPA of the LCC-Series Topology

Based on the analysis in Section II-D, the *LCC*-series topology should be equivalent to Fig. 6(b) to investigate its load-independent CV output mode and ZPA condition. Fig. 6(b) consists of two-stage reverse *L*-section networks and two-stage normal *L*-section networks in series. From Figs. 2(b)–6(b), the equivalent variables are expressed by the following:

$$j\omega_{CV}C_{pp} = \frac{1}{jX_{ppV1}} + \frac{1}{jX_{ppV2}} \quad (35a)$$

$$\frac{1}{j\omega_{CV}C_{ps}} + j\omega_{CV}(L_p - M) = jX_{psV1} + jX_{psV2} \quad (35b)$$

$$\frac{1}{j\omega_{CV}M} = \frac{1}{jX_{mV1}} + \frac{1}{jX_{mV2}} \quad (35c)$$

$$j\omega_{CV}(L_s - M) + \frac{1}{j\omega_{CV}C_{ss}} = jX_{ssV} \quad (35d)$$

where ω_{CV} ($\omega_{CV} = 2\pi f_{CV}$) represents the resonant frequency of Fig. 6(b).

According to (24) and (25), when

$$\begin{aligned} \omega_{CV}L_{ps} + X_{ppV1} &= X_{ppV2} + X_{psV1} = X_{psV2} + X_{mV1} \\ &= X_{mV2} + X_{ssV} = 0 \end{aligned} \quad (36)$$

the load-independent G_{CV} is derived as

$$G_{CV} = \frac{V_{abCV}}{V_{AB}} = \frac{jX_{ppV2} jX_{mV2}}{j\omega_{CV}L_{ps} jX_{psV2}}. \quad (37)$$

In addition, to ensure ZPA operation in the CV mode, the following resonant condition should be satisfied according to (28):

$$(X_{ppV1} + X_{ppV2})X_{psV2}X_{mV1} + X_{ppV2}X_{psV1}(X_{mV1} + X_{mV2}) = 0 \quad (38)$$

and according to (29), the purely resistive input impedance R_{inCV} is expressed as

$$R_{inCV} = \text{Re}(Z_{inCV}) = \frac{R_{ac}\omega_{CV}^2 L_{ps}^2 X_{psV2}^2}{X_{ppV2}^2 X_{mV2}^2} = \frac{R_{ac}}{|G_{CV}^2|}. \quad (39)$$

IV. EVALUATIONS

The proposed analysis methodology was verified with a 3.3-kW *LCC*-series-compensated IPT prototype. Fig. 7 shows the experimental setup. The air gap between the coupled coils is set at 20 cm for practical wireless EV charging applications. The input and output dc voltages are 400 and 320 V,

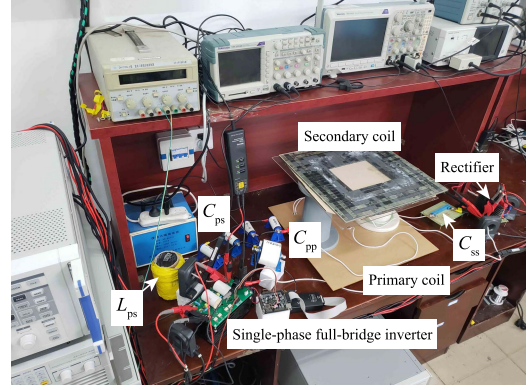


Fig. 7. Experimental setup of the IPT system.

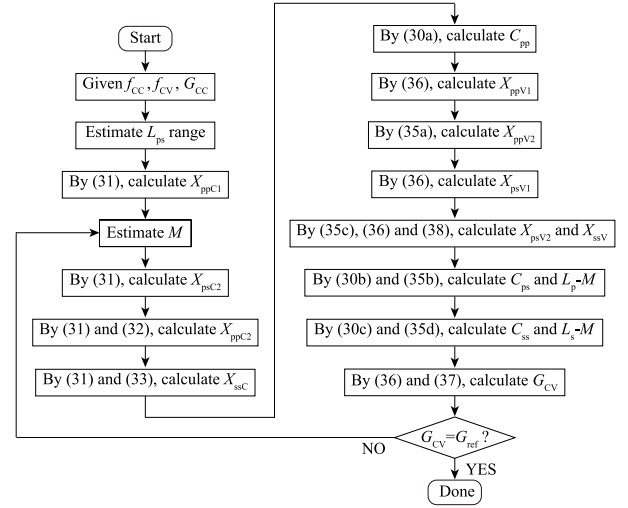


Fig. 8. Parameter design procedure of the *LCC*-series topology.

respectively, and the output dc current is 10.3125 A, which conforms with the SAE J2954 standard [36]. Using the fundamental harmonic approximation (FHA) method, the ac transconductance gain (G_{CC}) and the ac voltage transfer ratio (G_{CV}) are then calculated and equal to 0.0318 and 0.8, respectively. The compensation components parameters and the resonant frequencies in CC and CV modes (f_{CC} and f_{CV}) are estimated through solving (30)–(33) and (35)–(38). In addition, the detailed design procedure is shown in Fig. 8. In addition, it should be pointed out that with the similar design procedure shown in Fig. 8, the system component parameters of arbitrary higher order resonant circuits including any compensation topologies in WPT systems can be designed by combining (11)–(13), (16), (17), (20), (24), (25), and (28). Then, the designed component values are shown in Table II. From Table II, the resonant frequencies in CC and CV modes are 90 and 82 kHz, respectively, which are also in compliance with SAE J2954 standard. Infineon IPW65R041CFD power MOSFETs are used to build the input single-phase full-bridge inverter. Also, Infineon IDP30E65D2 diodes are adopted at the secondary-side single-phase full-wave bridge rectifier.

Fig. 9 shows the simulation results of the *LCC*-series topology versus frequency. It can be seen that the transconductance

TABLE II
SYSTEM PARAMETERS

Parameters	Value	Parameters	Value
V_{in}	400 V	V_o	320 V
I_o	10.3125 A	L_p	453 μ H
L_s	453 μ H	k	0.142
L_{ps}	120.40 μ H	C_{pp}	46.91 nF
C_{ps}	10.11 nF	C_{ss}	7.83 nF
f_{CC}	90 kHz	f_{CV}	82 kHz

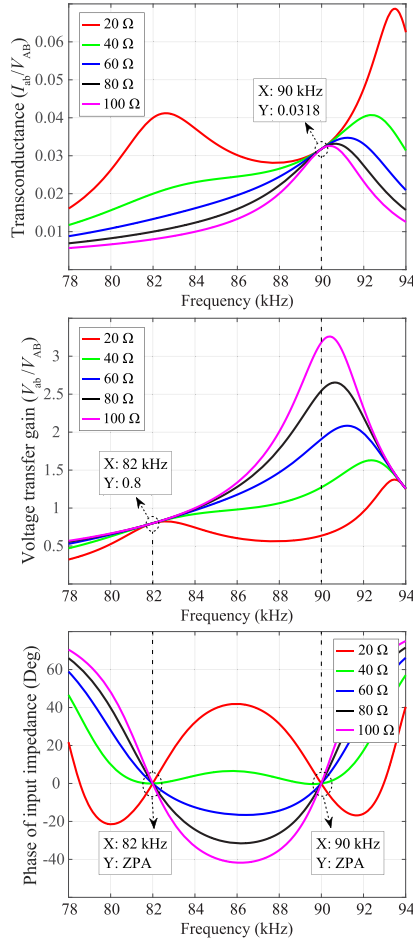


Fig. 9. Simulation results of G_{CC} , G_{CV} , and the phase of input impedance.

is independent of the load, and the input impedance is purely resistive at any load conditions when the topology resonates at 90 kHz. The simulated transconductance is 0.0318, which is equal to the designed value. So, the load-independent CC mode with ZPA is achieved. However, the load-independent CV mode with ZPA is obtained at 82 kHz. The simulation results demonstrate the theoretical analysis in Sections III-A and III-B.

The measured transient waveforms of the output voltage $v_{ab}(t)$ and current $i_{ab}(t)$ of the compensation topology and also the rectifier output dc voltage $v_o(t)$ and current $i_o(t)$ are shown in Fig. 10(a) at 90 kHz. Fig. 10(b) shows the transient waveforms of $v_{ab}(t)$, $i_{ab}(t)$, $v_o(t)$, and $i_o(t)$ at 82 kHz. From Fig. 10(a), $i_o(t)$ and $i_{ab}(t)$ are always

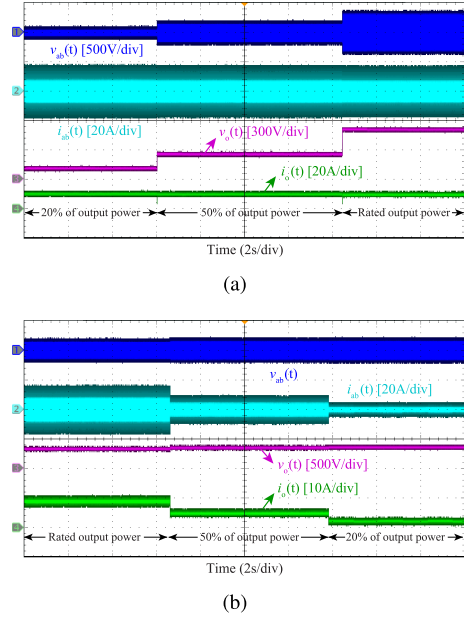


Fig. 10. Transient waveforms of $v_{ab}(t)$, $i_{ab}(t)$, $v_o(t)$, and $i_o(t)$ in (a) CC output mode and (b) CV output mode.

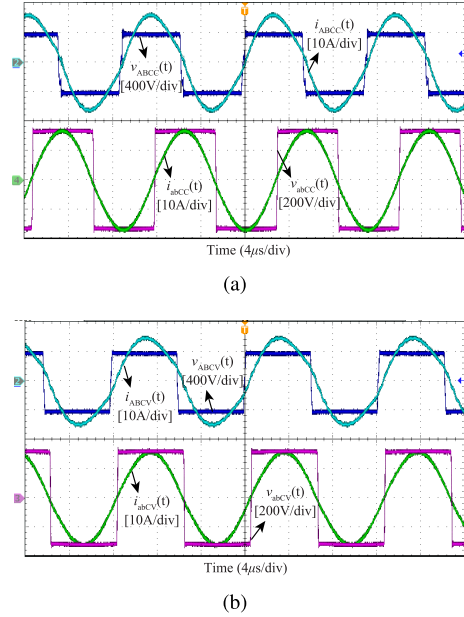


Fig. 11. Experimental waveforms of $v_{AB}(t)$, $i_{AB}(t)$, $v_{ab}(t)$, and $i_{ab}(t)$ at full-load power and in (a) CC output mode and (b) CV output mode.

constant at the load resistance of 6.21 (20% of full-load power), 15.52, and 31.03 Ω (full-load power). It means that the load-independent current output is achieved. However, from Fig. 10(b), the CVs $v_o(t)$ and $v_{ab}(t)$ are achieved at any load conditions.

Fig. 11(a) and (b) shows the experimental waveforms of $v_{AB}(t)$, $i_{AB}(t)$, $v_{ab}(t)$, and $i_{ab}(t)$ at 90 (CC mode) and 82 kHz (CV mode), respectively. Fig. 12(a) and (b) shows the experimental results of $v_{AB}(t)$, $i_{AB}(t)$, $v_{ab}(t)$, and $i_{ab}(t)$ when the system operates at 50% of full-load power and CC and CV modes. From Figs. 11(a) and 12(a), $i_{AB}(t)$ is almost in phase

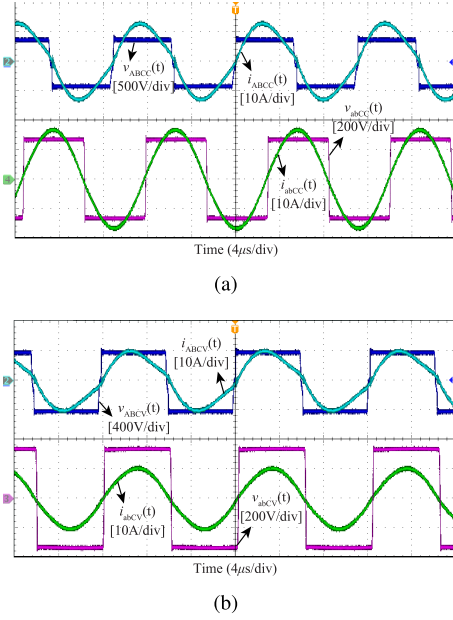


Fig. 12. Experimental waveforms of $v_{AB}(t)$, $i_{AB}(t)$, $v_{ab}(t)$, and $i_{ab}(t)$ at 50% of full-load power and in (a) CC output mode and (b) CV output mode.

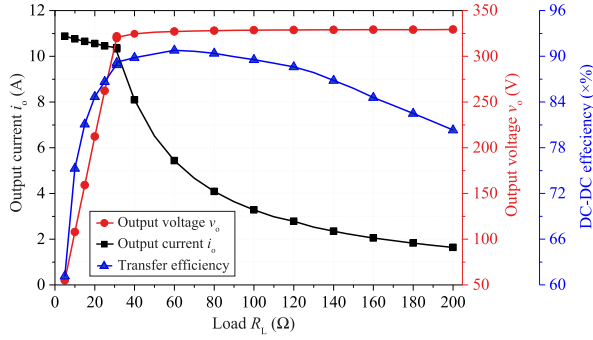


Fig. 13. Measured output dc current and voltage and the dc-dc efficiency over the full range of charging process.

with $v_{AB}(t)$. So, according to Figs. 10(a), 11(a), and 12(a), the load-independent CC output with ZPA is achieved for the *LCC*-series topology. Moreover, from Figs. 11(b) and 12(b), $i_{AB}(t)$ is also almost in phase with $v_{AB}(t)$, which match well with the analysis of (39). It means that the load-independent CV output with ZPA is obtained at 82 kHz.

Fig. 13 shows the output dc current and voltage and also the efficiency from the dc input of the inverter to the load over the full range of the charging process. The system operates at 90 kHz when the load resistance is continuously increased from 5 to 31.03 Ω , while the system operates at 82 kHz when the load increases from 31.03 to 200 Ω . It can be seen that the output current value is almost constant at 90 kHz and the load-independent CV output is achieved at 82 kHz. The maximum fluctuation of the output charging current in the CC mode is within 4.9%, and the maximum fluctuation of the output charging voltage is only 3.8% in the CV mode. The efficiencies in the CC and CV modes are 88.9% and 89.2% at the rated load condition. Furthermore, a peak system efficiency of 90.8% is obtained at 60 Ω and 90 kHz. Fig. 14 shows the

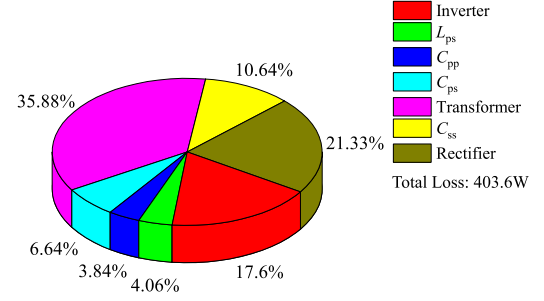


Fig. 14. Loss breakdown at full-load power at 82 kHz.

power loss breakdown based on the experimental results. The transformer loss is up to about 144.8 W, accounting for 35.88% of total losses. The large transformer loss is caused by the small coupling coefficient and the large parasitic resistances of the coupled coils.

V. CONCLUSION

This article proposed a general unified methodology for arbitrary higher order resonant circuits to analyze the both the load-independent CC and CV outputs with load-independent ZPA operations. The regularized mathematical models of the constant output current and voltage and the purely resistive input impedances are derived to simplify the design and optimization of the system parameters. The proposed method without adding any control algorithm and any auxiliary circuit can be generalized to investigate the load-independent output and input characteristics of some widely used resonant compensation topologies in WPT systems, including, but not limited to, *LCC*-series, *S-SP*, and double-sided *LCC*-compensated IPT systems, and double-sided *LC*, double-sided *LCLC*, and double-sided *CLLC*-compensated CPT systems. A design guideline for the practical wireless EV charging application of the *LCC*-series-compensated IPT system is experimentally verified by a 3.3-kW prototype.

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